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1101100975420	87
1101100975420	Sheets
1101100975420	Class
1101100975420	ISSUE CLASSIFICATION

PATENT NUMBER  
1

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED NSI G.A. 9W	

APPLICATION NO. - 09/975420	CONT/PRIOR - F	CLASS - 257 700	SUBCLASS - 87 + -	ART UNIT - 2911 2101	EXAMINER - Shute
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Jong-Keun Ahn

Method and device for programming nonvolatile semiconductor memory

TITLE

PTO-2040  
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
700	87	700	18	86	121
INTERNATIONAL CLASSIFICATION		711	102	103	
G05SB 19/42		365	189.04	189.05	219
		710	52	53	71
		326	37	38	
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	5	5	1	17	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Douglas Shute 2/10/05 (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	
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